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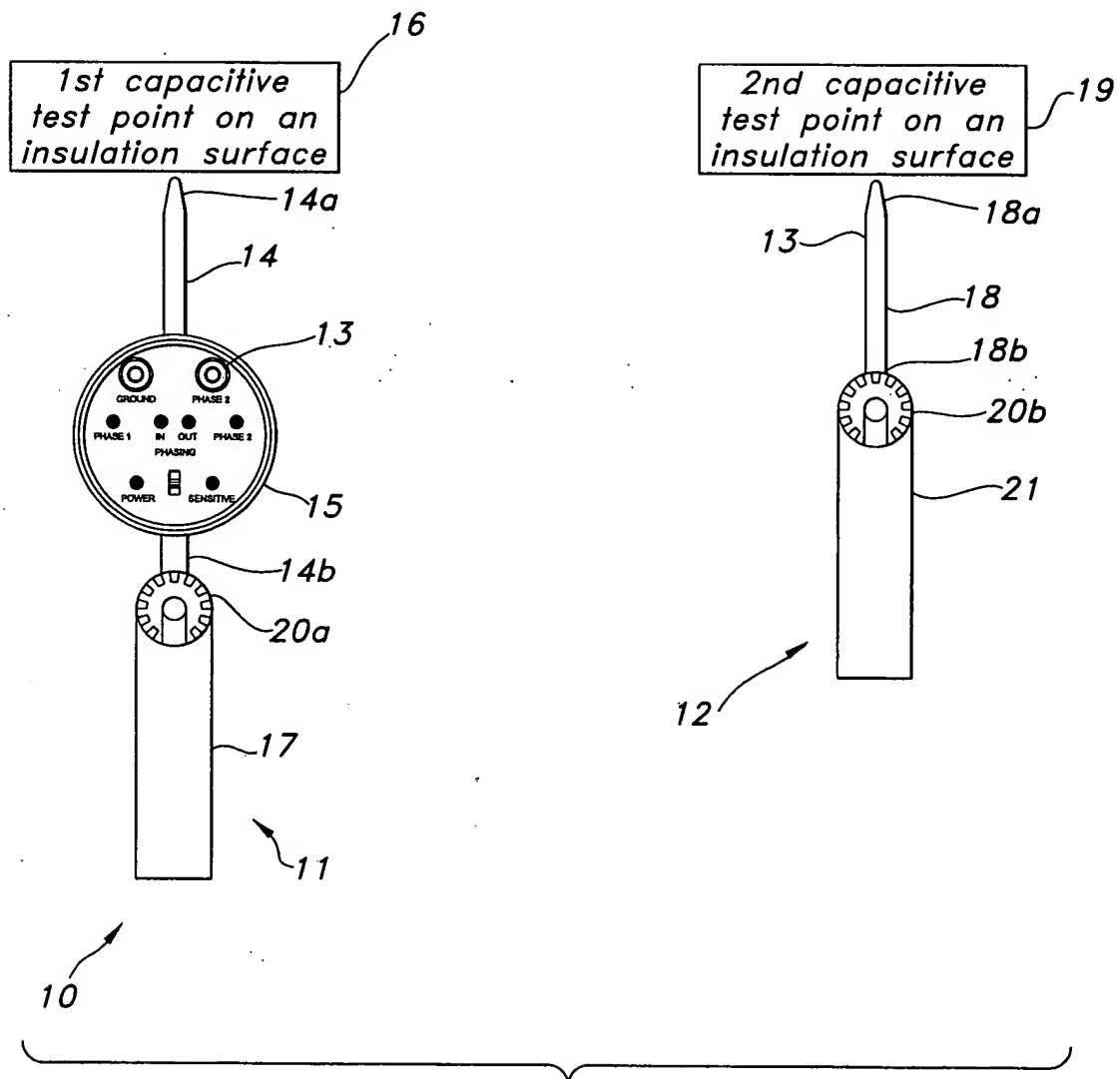


FIG 1

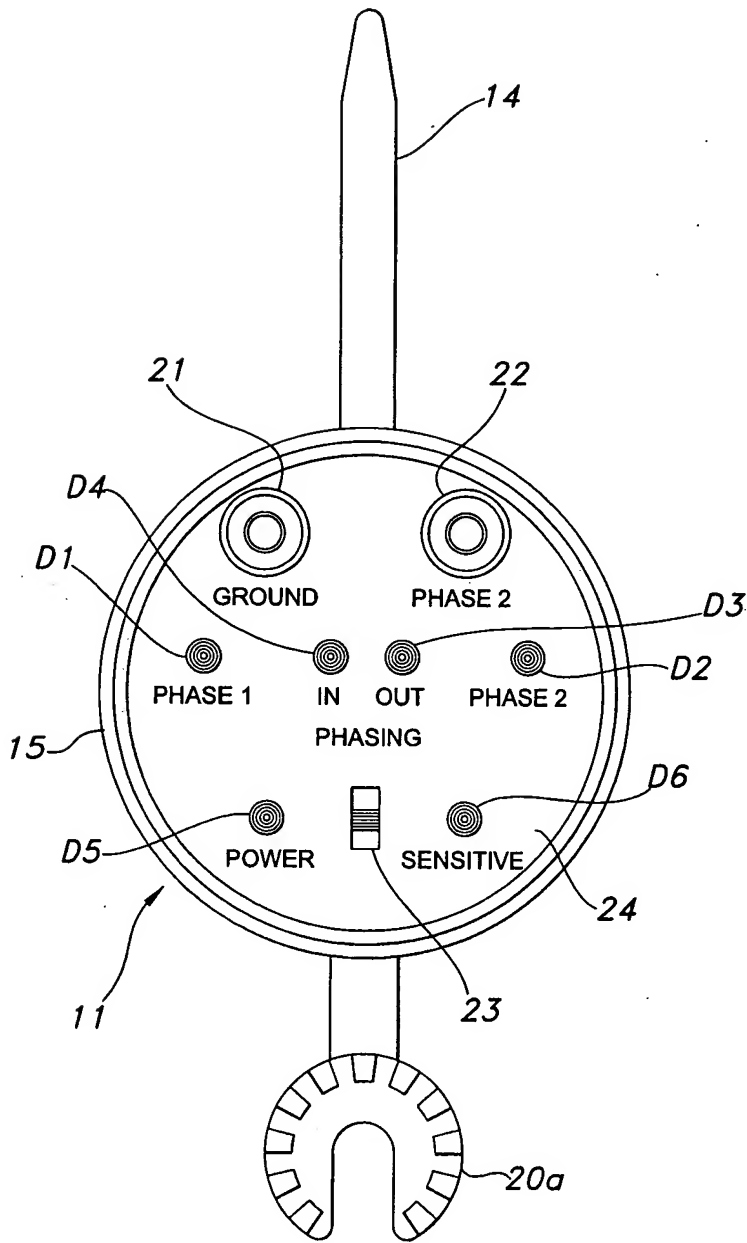


FIG 2

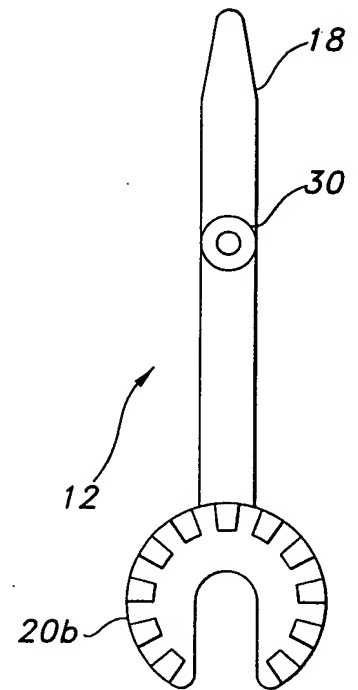


FIG 3

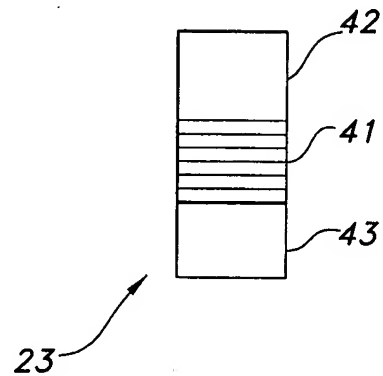


FIG 4

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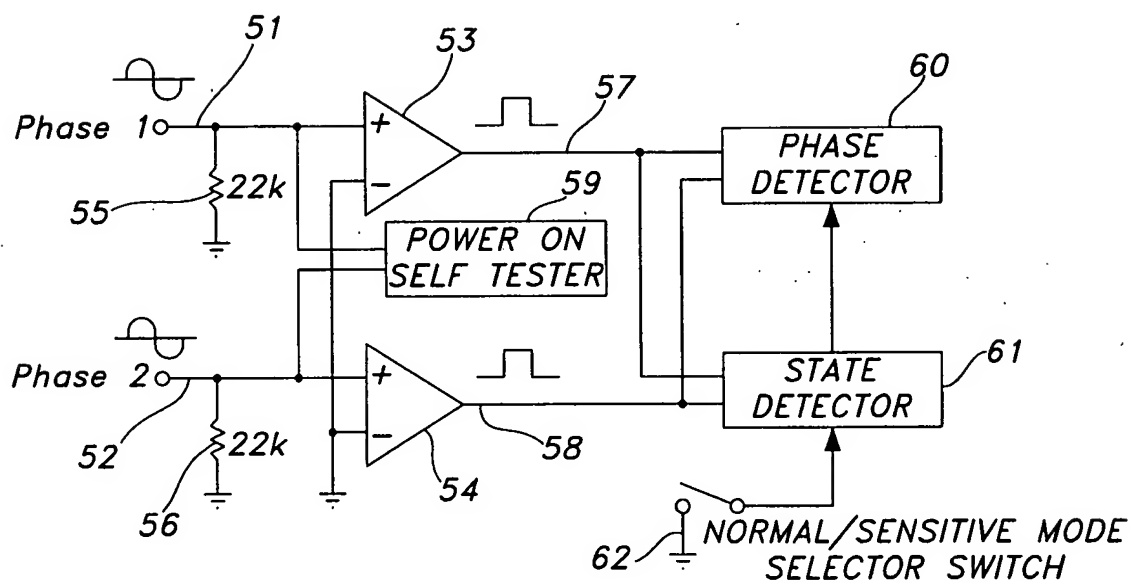


FIG 5

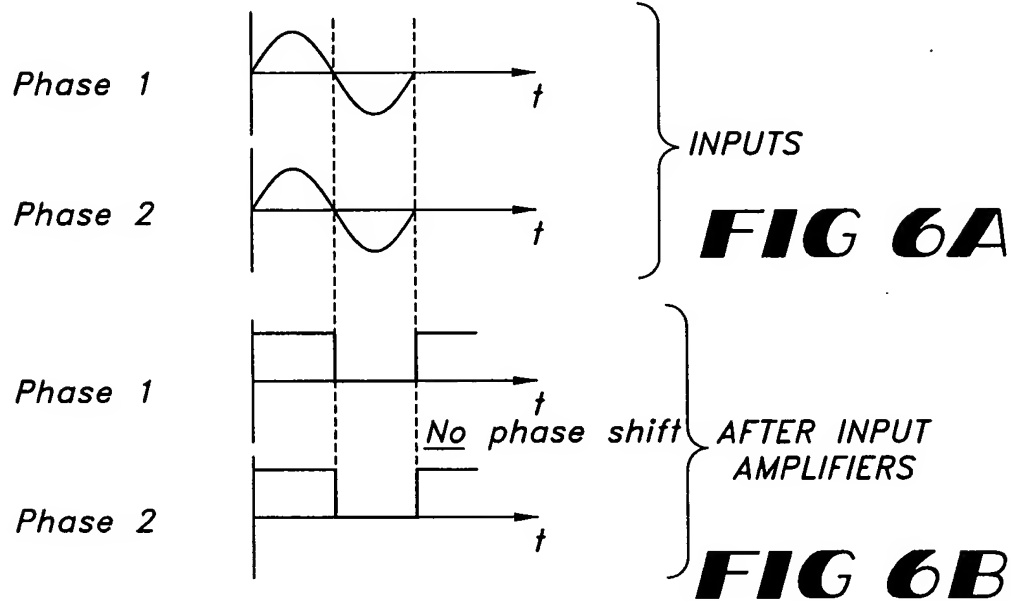
Title: CAPACITIVE TEST POINT VOLTAGE AND PHASING DETECTOR

Applicant: Timothy R. Taylor

Application No.: 10/033,118

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WAVEFORMS FOR IN-PHASE:



WAVEFORMS FOR OUT-OF-PHASE:

